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APPLICANT

Eliyahou Harari et al.

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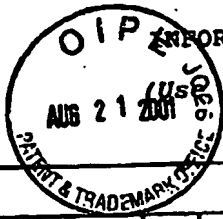
May 30, 2001

GROUP

2824

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)



## U. S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
AT	A32 4 1 8 1 9 8 0	01/1980	McCoy			
	A33 4 2 7 2 8 3 0	06/1981	Moench			
	A34 4 2 8 7 5 7 0	09/1981	Stark			
	A35 4 5 8 6 1 6 3	04/1986	Koike			
	A36 4 6 5 3 0 2 3	03/1987	Suzuki et al.			
AT	A37 4 8 4 7 8 0 8	07/1989	Kobatake			

## FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANS.? (YES/NO)

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

AT	C21	Stark, M., "Two Bits Per Cell Rom," IEEE Catalog No. 81-CH1626-1, Library of Congress No. 80-85186, Order No. 341, Spring Comp Con 1981, Feb. 23-26, Twenty-Second Computer Society International Conference, San Francisco, CA
	C22	Furuyama et al., "An Experimental 2-Bit/Cell Storage Dram For Macro Cell Or Memory-On-Logic Application," pp. 4.4.1 - 4.4.4, IEEE, (1988)
AT	C23	Horiguchi et al., "An Experimental Large-Capacity Semiconductor File Memory Using 16 Levels Cell Storage," IEEE Journal Of Solid-State Circuits, Vol. 23, No. 1, February 1988

EXAMINER

/Andrew Tran/

DATE CONSIDERED

04/27/2006

\* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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